

GaAs PD Epiwafer

(LD) AND LIGHT-EMITTING DIODE (LED)

EPIWAFERS FOR VERTICAL CAVITY SURFACE EMITTING LASER DIODE (VCSEL)

SOLAR CELL EPIWAFERS

Descriptions

Two- or three-inch epiwafers grown by MOVPE are available for GaAs photodiode (PD) fabrication. An epiwafer layer structure is shown in **Figure 1**. The top window layer can be either AlGaAs or InGaP which has larger energy band-gap than GaAs. From the structure, Zn-diffusion method is used by customers to convert the top layer material into P-type, by exposing wafers to Zn vapor through SiN defined windows. This type of photodiode is called the planar P-i-N PD. For ease of P-ohmic contact after Zn-diffusion process, a thin U-GaAs layer can be grown on top of the window layer. LandMark also provides the P-AlGaAs or P-InGaP on top of i-GaAs for etched mesa-type P-i-N PD. Also, the substrate can be Semi-Insulating (SI) type for PD array or high speed PD fabrication. **Figure 2** shows the schematic bird's view and cleaved side-view of a PD chip. The GaAs PD is widely used in the Gigabit Ethernet receiver or transceiver where laser's wavelength is shorter than 870 nm.

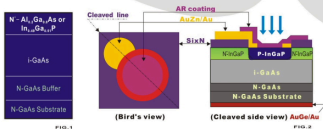


FIG. 1

FIG. 2

Wafer Characterization

Epiwafers are characterized by DCXD, Alpha-Step, PL and E-CV tests. **Figure 3**. Shows the X-ray rocking curve of an InGaP/GaAs PD wafer. Typically, LandMark can control the lattice mismatch of InGaP material within 500ppm with ± 150 ppm uniformity across a 2-inch wafer.

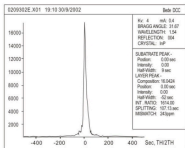


FIG. 3

GaAs PD Epiwafer

LandMark

EPIWAFERS FOR PHOTODETECTOR (PD) EPIWAFERS FOR VISIBLE-LIGHT LASER DIODE (LD) and RCLD EPIWAFERS FOR LASER DIODE

Wafer Characterization

Figure 4 shows the carrier concentration depth profile from GaAs/Buffer/Substrate layers. It is measured by the depletion-mode CV method. The intrinsic GaAs material has background concentration as low as $5-1.0 \times 10^{14} \text{ cm}^{-3}$.

The precise thickness of the top InGaP layer is necessary for customers doing Zn-diffusion. Because, where the Zn-diffusion-front position to InGaP/GaAs interface is critical for response and capacitance characteristics. Hence, LandMark provides thickness data of the layer measured by Alpha-step machine. **Figure 5** shows how the thickness of the layer is being measured. The sample under test had been wet-etched to reveal the InGaP to GaAs step. The probe goes through the step and measures the thickness.

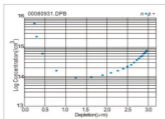


FIG. 4

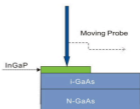


FIG. 5

Typical Epitaxial Parameters

Parameters	Values
Thickness control	Better than $\pm 5\%$
Thickness uniformity	Better than $\pm 2\%$ at the inner 40mm
PL Wavelength uniformity	$\sim 870 \text{ nm}$, no variation is observed
Doping control	$\sim 20\%$ for P-type; $\sim 20\%$ for N-type
Zn diffused P-InGaP (cm^{-3})	Zn doped, 1×10^{17} to 3×10^{18}
N-GaAs doping (cm^{-3})	Si doped, 1×10^{17} to 5×10^{18}
P-A I-GaAs doping (cm^{-3})	C doped, $> 1 \times 10^{18}$
U-InGaP background C.C. (Cm^{-3})	5×10^{15} – 2×10^{16}
I-GaAs background C.C. (Cm^{-3})	$< 5 \times 10^{14}$; Minimum $\sim 8 \times 10^{13}$
Defect density control (Diameter)	$< 50 \text{ cm}^{-2}$ ($\text{D} > 10 \mu\text{m}$)

Typical Device Performance

Parameter	Symbol	Typical
Dark current @ -5V	I_d	$< 100 \text{ pA}$
Capacitance @ -5V	C_j	$\sim 0.85 \text{ pF}$
Responsivity	R_{es}	$\sim 0.65 \text{ A/W}$ (@850nm)
Break down Voltage @ 10uA	V_b	$\sim 20 \text{ V}$
Serial resistance	R_s	$< 50 \text{ } \Omega$
Good device yield	--	$> 80\%$
Zn diffused, planar type PIN		100 μm aperture